

# CALL FOR PAPERS

## The Fifth Asian Test Symposium (ATS'96)

**November 20-22, 1996**  
**National Tsing Hua University**  
**Hsinchu, Taiwan**

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### **Objective:**

ATS'96 will follow the tradition that has been established by its predecessors, and continue to explore current concepts and future trends in design and testing of electronic circuits and systems. Although many of its attendees will be from Asian countries, it aims to attract papers from around the world. The Symposium provides a common meeting place for all who have an interest in the test community. It will cover theoretical and practical topics, and also topics that bridge theory and practice.

### **Scope:**

Papers addressing original and unpublished research contributions on theoretical and/or practical aspects of electronic testing are welcome. Specially sought will be papers that illuminate connections between practice and theory. Topics of interest include, but are not limited to:

- (A) test pattern generation,
- (B) fault simulation,
- (C) design for testability,
- (D) synthesis for testability,
- (E) built-in self-test,
- (F) circuit and system level diagnostics,
- (G) functional level testing,
- (H) switch level testing,
- (I) fault tolerance,
- (J) concurrent error detection,
- (K) analog and mixed-signal testing,
- (L) memory testing,
- (M) Iddq testing,
- (N) board and system level testing,
- (O) test economics,
- (P) software test.

## **Submissions:**

Authors are invited to submit **five (5)** copies of a full paper (in English, 6-15 pages double spaced), **twenty (20)** copies of a one page abstract, and a separate cover page to the **Technical Program Chair**. The cover page must contain or identify 1) the title of the paper, 2) the name and affiliation of each author, 3) a classification of the topic covered (using one of the topics listed above or creating one if necessary), 4) the principal author (including his/her e-mail address and fax number if available), and 5) the following signed statement: "All appropriate clearances for the publication of this paper have been obtained, and if accepted the author(s) will prepare the final manuscript in time for inclusion in the Symposium Proceedings and will present the paper at the Symposium."

## **About Taiwan and Hsinchu:**

[Taiwan](#) is an island with 36,000 square kilometers of land, of which about 70% is covered by spectacular mountains. The average temperature in Hsinchu area in November is around 19 degrees C (66 degrees F). [Hsinchu](#) is located about 64 kilometers (40 miles) south of Taipei (a 1-hour drive), and about 48 Kilometers (30 miles) south of the CKS International Airport (a 45-minute drive). The city's population is about 400,000. It is the home of [National Tsing Hua University](#), [National Chiao Tung University](#), the [Industrial Technology Research Institute](#), the [Science-Based Industrial Park](#), and the [Chip Implementation Center](#), which makes it the heart of the rapidly growing electronics and information industries in Taiwan.

## **Important Dates:**

- Deadline for Submission: March 15, 1996
- Notification of Acceptance: June 15, 1996
- Deadline for Receipt of Camera-ready Copies: August 1, 1996
- Tutorials: November 20, 1996
- Symposium: November 21-22, 1996

## **General Chair:**

Professor Chung-Len Lee  
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National Chiao Tung University  
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## **Technical Program Chair:**

Professor Cheng-Wen Wu  
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*Cheng-Wen Wu*

*Thu May 25 23:00 CST 1995*